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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Group Art Unit: 2111

Examiner. Phan

Applicant(s): Dorr

Application No.: 10/673305

Filed: 09/29/2003

Title: Dual Master Dual Serial Bus Architecture

Attorney Docket No.: 130-018

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE UNDER 37 CFR 1.111

Dear Sir:

In response to Office Action dated September 27, 2005, the above-identified application is amended as follows.

Amendments to the claims are reflected in the listing of claims beginning on page 2 of this рарет.

Remarks begin on page 12 of this paper.